

AMENDMENTS TO THE SPECIFICATION

Please replace paragraph [1032] with the following amended paragraph:

[1032] Referring to Fig. 4, in another embodiment the option is provided for using one of two dedicated I/Os on the integrated circuit device. The P1 port 81 is a dedicated I/O that functions as a bidirectional serial port for register data reads and writes, and as a calibration clock input, similar to the function of the OE pin used for programming and calibration described above but without any OE pin functionality. The P2 port 82 is also a dedicated I/O with the same serial bus and calibration clock functionality as P1; however, once programming is completed, P2 can be converted from a dedicated serial port I/O to an input control for the output enable function. Until the dedicated I/O functionality is disabled on P2, there is no output enable pin functionality provided by P2. At the initiation of manufacture test, before the on-chip non-volatile memory (NVM) has been written, either P1 and P2 are able to receive serial bus and calibration clock signals as dedicated I/Os (both should not be used at the same time). At the completion of the manufacture test programming of the various programmable registers, the user may write the programmable register values into non-volatile memory. P2 can then be programmed to function as an output enable control input causing its dedicated programmable I/O functionality to be permanently disabled. That may be accomplished by writing a keyword byte to a programmable register (activate output enable register) to turn on the output enable functionality of the P2 pin and terminate the dedicated serial I/O functionality of the P2 I/O. That may be accomplished using logic gates enabling one function and disabling the other function according to the ~~active~~ activate output enable register value. P1 always is available to function as a serial and calibration clock port.

Please replace paragraph [1037] with the following amended paragraph:

[10037] Fig. 6 [[gives]] illustrates a Manchester encoded "0" and "1" and also shows the required preamble data pattern. Note that the preamble contains multiple Manchester code violations in order to increase its uniqueness and reduce the chances of false preamble detection.